

Department of Electronics & Communication Engg.(PESCE – Mandya)
TIME – TABLE ODD SEM: (Reopening: 01.09.2010 Closing ())

Semester: III Branch: E&C Class Room:AEL-202 Month: SEPT Year:2010

Time	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
MON		3	4	L U N C H T I M E	6	2	5	L U N C H T I M E	←--- ←---	--- 7 --- -----8--	-> L2 ->L4
TUE		6	5		1	4	2		←--- ←---	Placem ent	----->
WED		3	6		5	2	1		←--- ←---	--- 7 --- -----8--	-> L4 ->L3
THU		2	4		5	3	1		←--- ←---	--- 7 --- -----8--	-> L1 ->L3
FRI		6	4		←--- ←---	--- 7 --- -----8--	->RM ->SJ		←--- ←---	--- 7 --- -----8--	-> L2 ->BSN
SAT		3	1		←--- ←---	--- 7 --- -----8--	->RM ->L2				

Index:

1.	ENGG.MATHEMATICS - III	5.	P08EC35 - ELECTRICAL CIRCUIT THEORY- HSS
2.	P08EC32 - TRANSISTOR CIRCUITS - MG	6.	P08EC36- MEASUREMENTS AND INSTRUMENTATION- RM
3.	P08EC33- DIGITAL DESIGN – L4	7.	P08EC37- TRANSISTOR CIRCUITS LAB
4.	P08EC34 - ENGINEERING ELECTROMAGNETICS - BSN	8.	P08EC38 - DIGITAL DESIGN LAB

Department of Electronics & Communication Engg.(PESCE – Mandya)
TIME – TABLE ODD SEM: (Reopening: 01.09.2010 Closing: ())

Semester: V Branch: E&C Class Room:ECL-202 Month: SEPT Year:2010

Time	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
MON		3	4	T E A T I M E	2	1	5	L U N C H T I M E	←-----7----- →MJA ←-----8----- →NMG		
TUE		3	6		4	5	2		←-----7-----→HSS ←-----8-----→RBD		
WED		5	6		3	2	4		←---PLACEMENT--->		
THU		2	1		4	5	3		←-----7-----→L4 ←-----8-----→BSN		
FRI		1	6		←-----7-----→L4 ←-----8-----→HSS				←-----7----- →MJA ←-----8-----→RBD		
SAT		6	1		←-----7-----→L3 ←-----8-----→BSN						

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1	P08EC51- ENTREPRENEURSHIP DEVELOPMENT AND MANAGEMENT –L3	5	P08EC55 - VLSI CIRCUITS AND DESIGN – L1
2	P08EC52 - DIGITAL SIGNAL PROCESSING-MJA	6	P08EC56- EMBEDDED SYSTEM DESIGN -NMG

3	P08EC53- INFORMATION THEORY AND CODING – L2	7	P08EC57- DSP LAB
4	P08EC54 - DIGITAL COMMUNICATION - RM	8	P08EC58 - BASIC COMMUNICATION LAB

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TIME – TABLE ODD SEM: (Reopening: 02.08.2010 Closing:

Semester: VII Branch: E&C Class Room: ECL-302 Month: AUG Year:2010

Time	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON		5	6	T E A T I M E	1	2	4	L U N C H T I M E	←-----7-B1-----→L3			
										←-----8-B2---→RM		
TUE		1	6			4	2		3	←-----7-B2-----→L3		
										←-----8-B1-----→L4		
WED		5	4			6	2		3	←-----7-B3----→L1		
										←-----8-B4--→NMG		
THU		6	1			3	2		5	←---PLACEMENT--→		
FRI		1	5		←-----7---B5----→L1			←-----7-B4-----→L3				
					←-----8---B6---→NMG			←-----8-B3-----→SJ				
SAT		3	4		←-----7---B6----→MJA							
					←-----8-B5-----→SJ							

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1.	06EC71 –COMPUTER COMMUNICATION NETWORKS – L1	5	06EC75 - ELECTIVE – 2(GROUP – B) – IMAGE PROCESSING - HSS
2.	06EC72-OPTICAL FIBER COMMUNICATION - KNM	6	06EC76 - ELECTIVE – 3(GROUP – C) – SPEECH PROCESSING - RBD
3.	06EC73 – POWER ELECTRONICS - SJ	7	06ECL77 – VLSI LAB
4.	06EC74 – DSP ALGORITHM & ARCHITRCTURE - MJA	8	06ECL78 – POWER ELECTRONICS LAB

Department of Electronic & Communication Engg.(PESCE – Mandya)
TIME – TABLE ODD SEM

Name:KNM

Branch: E&C

Month: SEPT

Year:2010

Time Day	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON				T E A T I M E		1		L U N C H T I M E				
TUE						1						
WED						1						
THU						1						
FRI												
SAT												

Index:

1.OPTICAL FIBER COMMUNICATION – VII SEM – ECL 302

Department of Electronic & Communication Engg.(PESCE – Mandya)
TIME – TABLE ODD SEM

Name:HSS

Branch: E&C

Month: SEPT

Year:2010

Time Day	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON		2		T E A T I M E			1	L U N C H T I M E				
TUE			1						←-----3-----→			
WED		2			1							
THU					1		2					
FRI			2			←-----4-----→						
SAT												

Index:

- | | |
|---|------------------------------------|
| 1. Electrical Ckt Theory – III sem – AEL202 | 4. Basic Communication Lab – V sem |
| 2. Image Processing – VII Sem – ECL 302 | |
| 3. DSP LAB- V Sem | |

Department of Electronic & Communication Engg.(PESCE – Mandya)

TIME – TABLE ODD SEM

Name:RBD

Branch: E&C

Month: SEPT

Year:2010

Time Day	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON			2	T E A T I M E		1		L U N C H T I M E				
TUE			2			1			←-----3-----→			
WED					2							
THU		2										
FRI		1								←-----3-----→		
SAT			1									

Index:

1. Electronics Fundamentals – II sem – F – ALH - 5
 - 2.SPEECH PROCESSING – VII SEM – ECL302
 - 3.BASIC COMMUNICATION LAB – V SEM
-

Department of Electronic & Communication Engg.(PESCE – Mandya)

TIME – TABLE ODD SEM

Name:SJ

Branch: E&C

Month: SEPT

Year:2010

Time Day	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON				T E A T I M E			1	L U N C H T I M E				
TUE					1		2					
WED			1				2					
THU					2					1		
FRI					←-----4-----→					←-----3-----→		
SAT		2			←-----3-----→							

Index:

1. Electronics Fundamentals – II sem – E – ALH - 4
2. POWER ELECTRONICS – ECL302- VII SEM
3. . POWER ELECTRONICS LAB – VII SEM
4. DIGITAL DESIGN LAB – III SEM

Department of Electronic & Communication Engg.(PESCE – Mandya)

TIME – TABLE ODD SEM

Name:BSN

Branch: E&C

Month: SEPT

Year:2010

Time Day	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15	T E A C H I N G T I M E	10.45 to 11.40	11.40 to 12.35	12.35 to 1.30	L U N C H T I M E	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON			2				1					
TUE			1			2						
WED		1										
THU			2				1			←-----3-----→		
FRI			2							←-----4-----→		
SAT						←-----3-----→						

Index:

1. . Electronics Fundamentals – II sem – G – ALH - 6
2. ENGG ELECTROMAGNETICS – III SEM – AEL202
3. BASIC COMMUNICATION LAB – V SEM
4. DIGITAL DESIGN LAB – III SEM

Department of Electronic & Communication Engg.(PESCE – Mandya)

TIME – TABLE ODD SEM

Name:RM

Branch: E&C

Month: SEPT

Year:2010

Time Day	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15	T E A C H I N G T I M E	10.45 to 11.40	11.40 to 12.35	12.35 to 1.30	L U N C H T I M E	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON			2			1				←-----4-----→		
TUE		1				2						
WED			1						2			
THU						2						
FRI		1				←-----3-----→						
SAT						←-----3-----→						

Index:

1. MEASUREMENTS AND INSTRUMENTATION- III SEM – AEL202
2. DIGITAL COMMUNICATION – V SEM – ECL202
3. TRANSISTOR CIRCUIT LAB – III SEM
4. POWER ELECTRONICS LAB – VII SEM

Department of Electronic & Communication Engg.(PESCE – Mandya)

TIME – TABLE ODD SEM

Name:NMG

Branch: E&C

Month: SEPT

Year:2010

Time	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON				T E A T I M E		1		L U N C H T I M E	←-----4-----→			
TUE			2				1					
WED			2				1			←-----3-----→		
THU		1										
FRI			2			←-----3-----→						
SAT		2										

Index:

- 1.TRANSISTOR CIRCUITS – III SEM – AEL - 202
- 2.EMBEDED SYSTEM DESIGN – V SEM – ECL -202
3. POWER ELECTRONICS LAB – VII SEM
4. BASIC COMMUNICATION LAB – V SEM

Department of Electronic & Communication Engg.(PESCE – Mandya)

TIME – TABLE ODD SEM

Name:MJA

Branch: E&C

Month: SEPT

Year:2010

Time	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON				T E A T I M E	1		2	L U N C H T I M E	←-----3-----→			
TUE					2		1					
WED			2				1					
THU		1										
FRI										←-----3-----→		
SAT			2			←-----4-----→						

Index:

- 1.DIGITAL SIGNAL PROCESSING – V SEM – ECL202
- 2.DSP ALGORITHM & ARCHITECTURE – VII SEM – ECL302
3. DSP LAB – V SEM

4. VLSI LAB – VII SEM

Department of Electronic & Communication Engg.(PESCE – Mandya)

TIME – TABLE ODD SEM

Name:L1

Branch: E&C

Month: SEPT

Year:2010

Time Day	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15	T E A T I M E	10.45 to 11.40	11.40 to 12.35	12.35 to 1.30	L U N C H T I M E	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON					2		1					
TUE		2				1						
WED		1								←-----3-----→		
THU			2				1			←-----4-----→		
FRI		2				←-----3-----→						
SAT												

Index:

1.VLSI CIRCUITS AND DESIGN – V SEM – ECL202

2.COMPUTER COMMUNICATION NETWORKS – VII SEM – ECL302

3.VLSI LAB – VII SEM

4.TRANSISTOR CIRCUITS LAB – III SEM

Department of Electronic & Communication Engg.(PESCE – Mandya)

TIME – TABLE ODD SEM

Name:L2

Branch: E&C

Month: SEPT

Year:2010

Time Day	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15	T E A T I M E	10.45 to 11.40	11.40 to 12.35	12.35 to 1.30	L U N C H T I M E	2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON		2								←-----3-----→		
TUE		2					1 ALH - 6					
WED						2					1 ALH - 6	
THU						1 ALH - 5			2			
FRI			1 ALH - 5							←-----3-----→		
SAT						←-----4-----→						

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1.ELECTONICS FUNDAMENTALS – II SEM – H

4. DIGITAL DESIGN LAB – III SEM

2. INFORMATION THEORY & CODING

3. TRANSISTOR CIRCUITS LAB – III SEM

Department of Electronic & Communication Engg.(PESCE – Mandya)

TIME – TABLE ODD SEM

Name:L3

Branch: E&C

Month: SEPT

Year:2010

Time	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON				T E A C H I N G T I M E		1		L U N C H T I M E	←-----2-----→			
TUE									←-----2-----→			
WED									←-----3-----→			
THU			1						←-----3-----→			
FRI		1							←-----2-----→			
SAT			1			←-----4-----→						

Index:

1. ENTREPRENEURSHIP DEVELOPMENT AND MANAGEMENT – V SEM – ECL202
- 2.VLSI LAB – VII SEM
- 3.DIGITAL DESIGN LAB – III SEM
4. DSP LAB – V SEM

Department of Electronic & Communication Engg.(PESCE – Mandya)

TIME – TABLE ODD SEM

Name:L4

Branch: E&C

Month: SEPT

Year:2010

Time	7.30 to 8.25	8.25 to 9.20	9.20 to 10.15		10.45 to 11.40	11.40 to 12.35	12.35 to 1.30		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15	
MON		1		T E A C H I N G T I M E				L U N C H T I M E	←-----3-----→			
TUE									←-----5-----→			
WED		1							←-----2-----→			
THU							1		←-----4-----→			
FRI						←-----4-----→						
SAT		1										

Index:

- 1.DIGITAL DESIGN – III SEM – AEL202
- 2.TRANSISTOR CIRCUITS LAB – III SEM
- 3.DIGITAL DESIGN LAB – III SEM
4. DSP LAB - VSEM
5. POWER ELECTRONICS LAB – VII SEM