

P.E.S COLLEGE OF ENGINEERING, MANDYA - 571 401

(An Autonomous Institution Affiliated to VTU, Belgaum)

Provisional Results of First Semester M.Tech Semester End Examination for the Academic year 2016-17

31-01-2017

ELECTRONICS AND COMMUNICATION ENGINEERING (VLSI Design and Embedded Systems)

SL.No.	USN	NAME	P15MECE11							P15MECE12							P15MECE13							P15MECE143							P15MECE151							P15MECE16							P15MECEL17							SGPA	CLASS							
			C	S	T	CR	GP	GR		C	S	T	CR	GP	GR		C	S	T	CR	GP	GR		C	S	T	CR	GP	GR		C	S	T	CR	GP	GR		C	S	T	CR	GP	GR		C	S	T	CR	GP	GR				C	S	T	CR	GP	GR	
1	4PS16LVS01	CHAITRA Y	37	20	57	4.0	7	C	39	28	67	4.0	8	B	39	36	75	4.0	9	A	43	9	52	0.0	0	F	46	36	82	4.0	9	A	38	0	76	2.0	9	A	44	40	84	2.0	9	A															7.00	FL
2	4PS16LVS02	DIMPLE R	35	24	59	4.0	7	C	36	34	70	4.0	8	B	43	37	80	4.0	9	A	47	31	78	4.0	9	A	45	38	83	4.0	9	A	38	0	76	2.0	9	A	46	42	88	2.0	9	A															8.50	FCD
3	4PS16LVS03	DIVYA M	47	32	79	4.0	9	A	45	41	86	4.0	9	A	44	37	81	4.0	9	A	48	30	78	4.0	9	A	49	43	92	4.0	10	S	41	0	82	2.0	9	A	46	45	91	2.0	10	S															9.25	FCD
4	4PS16LVS04	KUSUMASHREE C J	49	22	71	4.0	8	B	41	27	68	4.0	8	B	43	41	84	4.0	9	A	49	32	81	4.0	9	A	49	37	86	4.0	9	A	45	0	90	2.0	10	S	46	45	91	2.0	10	S															8.83	FCD
5	4PS16LVS05	PALLAVI K R	45	26	71	4.0	8	B	39	43	82	4.0	9	A	46	24	70	4.0	8	B	46	29	75	4.0	9	A	48	37	85	4.0	9	A	45	0	90	2.0	10	S	46	42	88	2.0	9	A															8.75	FCD
6	4PS16LVS06	PAVITHRA M	34	14	48	0.0	0	F	34	15	49	0.0	0	F	36	28	64	4.0	8	B	34	26	60	4.0	8	B	39	27	66	4.0	8	B	41	0	82	2.0	9	A	44	39	83	2.0	9	A															5.50	FL
7	4PS16LVS07	RAKSHITH S N	111	111	0	0.0	0	N	6	111	6	0.0	0	N	32	111	32	0.0	0	G	37	111	37	0.0	0	G	31	111	31	0.0	0	G	0	111	0	0.0	0	N	0	111	0	0.0	0	N																FL
8	4PS16LVS08	SHANKAREGOWDA H S	47	27	74	4.0	8	B	44	39	83	4.0	9	A	44	25	69	4.0	8	B	43	29	72	4.0	8	B	43	32	75	4.0	9	A	40	0	80	2.0	9	A	45	43	88	2.0	9	A															8.50	FCD
9	4PS16LVS09	SPOORTHY M N	43	27	70	4.0	8	B	42	25	67	4.0	8	B	44	28	72	4.0	8	B	46	22	68	4.0	8	B	47	30	77	4.0	9	A	42	0	84	2.0	9	A	45	43	88	2.0	9	A															8.33	FCD
10	4PS16LVS10	SRINIDHI GOWDA	38	23	61	4.0	8	B	28	30	58	4.0	7	C	32	32	64	4.0	8	B	41	29	70	4.0	8	B	42	34	76	4.0	9	A	38	0	76	2.0	9	A	45	40	85	2.0	9	A															8.17	FCD
11	4PS16LVS11	SWEETLIN PRATHIBA S	45	22	67	4.0	8	B	37	30	67	4.0	8	B	45	33	78	4.0	9	A	47	30	77	4.0	9	A	43	34	77	4.0	9	A	40	0	80	2.0	9	A	45	40	85	2.0	9	A															8.67	FCD

P15MECE11 : - CMOS VLSI Design

P15MECE12 : - Advanced Embedded System

P15MECE13 : - SOC Design

P15MECE143 : - Digital System Design Using Verilog

P15MECE151 : - ASIC Design

P15MECE16 : - Seminar

P15MECEL17 : - VLSI Design Lab

C - CIE, S - SEE, T - CIE+SEE, CR -Credits, GP - Grade Point, GR- Grade, 111 - Absent

Transitional Grades: I - Course Incomplete

W - Withdrawn from the Course N - Not Eligible w.r.t either CIE / Attendance

G -Satisfactory CIE and Attendance but Absent for SEE without Permission

FCD - First Class with Distinction, FC - First Class, SC - Second Class, PC - Pass Class, FL - Fail

Percentage Equivalence of Grade Points (10 - Point Scale)

SGPA	Percentage of Marks / Class (SGPA - 0.75) x 10	Level	Out-standing	Excellent	Very Good	Good	Average	Poor	Fail
5.75	50 (Second Class)	Grade	S	A	B	C	D	E	F
6.25	55	Grade Points	10	09	08	07	05	04	00
6.75	60 (First Class)	Raw Score	90 Above	89-75	74-60	59-50	49-45	44-40	Below 40
7.25	65								
7.75	70 (Distinction)								
8.25	75								



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Note : Candidates are informed to bring to the notice of COE for any discrepancy, if any, within three days from the date of announcement of results. Subsequent requests will not be entertained.

